ABSTRACT

A system is provided with the ability to throttle one or more hardware

5   elements of the system to reduce power consumption of the one or more

hardware elements, in response to an AC absence condition. In one

embodiment, the system is further provided with the ability to delay suspending

the system to memory in response to the AC absence condition. Further, the

system is provided with the ability to return the one or more hardware elements

10   to their normal power consumption, and cancel the delayed suspending of the

system to memory, if AC returns while the system is still active.